



A collection of circuits a microprocessor might use.
 It interrupts CMOS RAM CHIP ENABLE during
 powerfails tokeep RAM from being corrupt.
 Watchdog timer support.
 Power fail monitoring.
 Controls power to CMOS RAM.

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| Title | | |
| MAX691, G24312, MICROPROCESSOR SUPERVISOR | | |
| Size | Document Number | Rev |
| A | Design 1928 | 1 |
| Date: | Sunday, August 22, 2021 | Sheet 1 of 1 |