



16K x 1 DRAM, Dynamic Random Access Memory. Memory is organized as 128 rows by 128 columns. Row Address Strobe select a row. Column Address Strobe selects a column. Each cell is just a capacitor that needs to be refreshed every once in a while. It is refreshed a row at a time. Such is the dynamic part of being DRAMK. 16K bytes of memory would require 8 such chips.

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| Title | | |
| G25754, MB8118 16K x 1 DRAM | | |
| Size | Document Number | Rev |
| A | Design 1858 | 1 |
| Date: | Monday, May 17, 2021 | Sheet 1 of 1 |