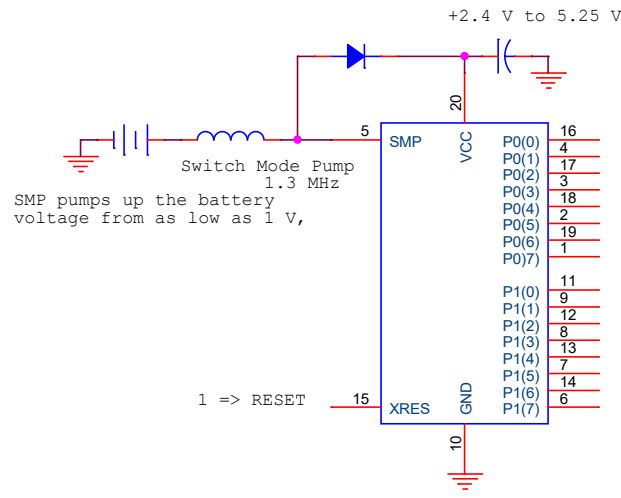


A PSoC is a step up from a microcontroller. It is a system on a chip. The CY8C24223A is a Harvard architecture processor running at up to 24 MHz. Harvard architecture implies that program memory is separate from data memory.



Switch Mode Pump
1.3 MHz
SMP pumps up the battery
voltage from as low as 1 V,

- 16 Ana In, Dig I/O
- 4 Ana In, Dig I/O
- 17 Ana In, Dig I/O
- 3 Ana I/O, Dig I/O
- 18 Ana In, Dig I/O
- 2 Ana I/O, Dig I/O
- 19 Ana In, Dig I/O
- 1 Ana In, Dig I/O
- 11 Dig I/O XTAL OUT, I2C SDA, ISSP SDEATA
- 9 Dig I/O XTAL IN, I2C SCL, ISSP SCOL
- 12 Dig I/O
- 8 Dig I/O
- 13 Dig I/O EXT CLK (32 KHz)
- 7 Dig I/O I2C, SDA
- 14 Dig I/O
- 6 Dig I/O I2C, SCL

1 => RESET

CY8C24223A

256 bytes of SRAM.
4K words of Flash program memory.
Clock frequency from 93 KHz to 3 MHz (VDD < 3 v)
93 KHz to 12 MHz (VDD 3 TO 4,75 V) or 93 KHz to
24 MHz at 5 V.

Title		
CY8C24223A, G26002		
Size	Document Number	Rev
A	Design 1902	1
Date:	Saturday, July 31, 2021	Sheet 1 of 1